

Docket No.: M4065.0699/P699-B
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Terry L. Gilton

Application No.: Not Yet Assigned

Filed: Concurrently Herewith

Art Unit: N/A

For: RESISTANCE VARIABLE DEVICE,
ANALOG MEMORY DEVICE AND
PRORAMMABLE MEMORY CELL

Examiner: Not Yet Assigned

INFORMATION DISCLOSURE STATEMENT (IDS)

MS Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Pursuant to 37 C.F.R. §§ 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached PTO/SB/08. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement accompanies the new patent application submitted herewith.

Those patents or publications which are marked with a double asterisk (**) next to the Cite No. in the attached form PTO/SB/08 are not supplied because they were previously cited by or submitted to the Office in a prior application number 10/264,677, filed October 3, 2002 and relied upon in this application for an earlier filing date under 35 U.S.C. § 120.

In accordance with 37 C.F.R. § 1.97(g), the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made. In accordance with 37 C.F.R. § 1.97(h), the filing of this Information Disclosure statement shall not be construed to be an admission that any patent, publication or other information referred to therein is "prior art" for this invention unless specifically designated as such.

It is submitted that the Information Disclosure Statement is in compliance with 37 C.F.R. § 1.98 and the Examiner is respectfully requested to consider the listed references.

The Director is hereby authorized to charge any deficiency in the fees filed, asserted to be filed or which should have been filed herewith (or with any paper hereafter filed in this application by this firm) to our Deposit Account No. 04-1073, under Order No. M4065.0699/P699-B. A duplicate copy of this paper is enclosed.

Dated: February 13, 2004

Respectfully submitted,

By _____

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Substitute for form 1449A/B/PTO				Complete if Known	
				Application Number	Not Yet Assigned
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				First Named Inventor	Terry L. Gilton
				Art Unit	N/A
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Sheet	1	of	14	Attorney Docket Number	M4065.0699/P699-B

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Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
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**	CR3	Guin, et al., "Indentation Creep of Ge-Se Chalcogenide Glasses Below Tg: Elastic Recovery and Non-Newtonian Flow," J. Non-Cryst. Solids, 298, 260-269, 2002.				
**	CS3	Guin, et al., "Hardness, Toughness, and Scratchability of Germanium-selenium Chalcogenide Glasses," J. Am. Ceram. Soc., 85, 1545-52, 2002.				
**	CT3	Gupta, "On Electrical Switching and Memory Effects in Amorphous Chalcogenides," J. Non-Cryst. Sol., 3, 148-154, 1970.				
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**	CV3	Haifz, et al., "Effect of Composition on the Structure and Electrical Properties of As-Se-Cu Glasses," J. Apply. Phys., 54, 1950-1954, 1983.				
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**	CX3	Hajto, et al., "DC and AC Measurements on Metal/a-Si:H/metal Room Temperature Quantised Resistance Devices," J. Non-Cryst. Solids, 266-269, 1058-1061, 2000.				
**	CY3	Hajto, et al., "Theory of Room Temperature Quantized Resistance Effects in Metal-a-Si:H-metal Thin Film Structures," J. Non-Cryst. Solids, 198-200, 825-828, 1996.				
**	CZ3	Hajto, et al., "Analogue Memory and Ballistic Electron Effects in Metal-amorphous Silicon Structures," Phil. Mag. B 63, 349-369, 1991.				
**	CA4	Hayashi, et al., "Polarized Memory Switching in Amorphous Se Film," Japan. J. Appl. Phys., 13, 1163-1164, 1974.				
**	CB4	Hegab, et al., "Memory Switching Phenomena in Thin Films of Chalcogenide Semiconductors," Vacuum, 45, 459-462, 1994.				
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**	CZ4	Hajto, et al., "Analogue Memory and Ballistic Electron Effects in Metal-amorphous Silicon Structures," Phil. Mag. B 63, 349-369, 1991.				
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				First Named Inventor	Terry L. Gilton
				Art Unit	N/A
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**	CR6	Pearson, et al., "Filamentary Conduction in Semiconducting Glass Diodes," App. Phys. Lett., 14, 280-282, 1969.			
**	CS6	Pinto, et al., "Electric Field Induced Memory Switching in Thin Films of the Chalcogenide System Ge-As-Se," Appl. Phys. Lett., 19, 221-223, 1971.			
**	CT6	Popescu, "The Effect of Local Non-uniformities on Thermal Switching and High Field Behavior of Structures with Chalcogenide Glasses," Solid-state Electronics, 18, 671-681, 1975.			
**	CU6	Popescu, et al., "The Contribution of the Lateral Thermal Instability to the Switching Phenomenon," J. Non-Cryst. Solids, 8-10, 531-537, 1972.			
**	CV6	Popov, et al., "Memory and Threshold Switching Effects in Amorphous Selenium," Phys. Stat. Sol. (a) 44, K71-K73, 1977.			
**	CW6	Prakash, et al., "Easily Reversible Memory Switching in Ge-As-Te Glasses," J. Phys. D: Appl. Phys., 29, 2004-2008, 1996.			
**	CX6	Rahman, et al., "Electronic Switching in Ge-Bi-Se-Te Glasses," Mat. Sci. and Eng. B12, 219-222, 1992.			
**	CY6	Ramesh, et al., "Electrical Switching in Germanium Telluride Glasses Doped With Cu and Ag," Appl. Phys. A 69, 421-425, 1999.			
**	CZ6	Rose, et al., "Amorphous Silicon Analogue Memory Devices," J. Non-Cryst. Solids, 115, 168-170, 1989.			
**	CA7	Rose, et al., "Aspects of Non-volatility in a -Si:H Memory Devices," Mat. Res. Soc. Symp. Proc. V 258, 1075-1080, 1992.			
**	CB7	Schuocker, et al., "On the Reliability of Amorphous Chalcogenide Switching Devices," J. Non-Cryst. Solids, 29, 397-407, 1978.			
**	CC7	Sharma, et al., "Electrical Conductivity Measurements of Evaporated Selenium Films in Vacuum," Proc. Indian Natn. Sci. Acad. 46, A, 362-368, 1980.			
**	CD7	Sharma, "Structural, Electrical and Optical Properties of Silver Selenide Films," Ind. J. Of Pure and Applied Phys., 35, 424-427, 1997.			
**	CE7	Snell, et al., "Analogue Memory Effects in Metal/a-Si:H/metal Memory Devices," J. Non-Cryst. Solids, 137-138, 1257-1262, 1991.			
**	CF7	Snell, et al., "Analogue Memory Effects in Metal/a-Si:H/metal Thin Film Structures," Mat. Res. Soc. Symp. Proc. V 297, 1017-1021, 1993.			
**	CG7	Steventon, "Microfilaments in Amorphous Chalcogenide Memory Devices," J. Phys. D: Appl. Phys., 8, L120-L122, 1975.			
**	CH7	Steventon, "The Switching Mechanisms in Amorphous Chalcogenide Memory Devices," J. Non-Cryst. Solids, 21, 319-329, 1976.			
**	CI7	Stocker, "Bulk and Thin Film Switching and Memory Effects in Semiconducting Chalcogenide Glasses," App. Phys. Lett., 15, 55-57, 1969.			

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**	CJ7	Tanaka, "Ionic and Mixed Conductions in Ag Photodoping Process," Mod. Phys. Lett B 4, 1373-1377, 1990.				
**	CK7	Tanaka, et al., "Thermal Effects on Switching Phenomenon in Chalcogenide Amorphous Semiconductors," Solid State Comm., 8, 387-389, 1970.				
**	CL7	Thornburg, "Memory Switching in a Type I Amorphous Chalcogenide," J. Elect. Mat., 2, 3-15, 1973.				
**	CM7	Thornburg, "Memory Switching in Amorphous Arsenic Triselenide," J. Non-Cryst. Solids, 11, 113-120, 1972.				
**	CN7	Thornburg, et al., "Electric Field Enhanced Phase Separation and Memory Switching in Amorphous Arsenic Triselenide," Journal(??), 4609-4612, 1972.				
**	CO7	Tichy, et al., "Remark on the Glass-forming Ability in GexSe1-x and AsxSe1-x Systems," J. Non-Cryst. Solids, 261, 277-281, 2000.				
**	CP7	Titus, et al., "Electrical Switching and Short-range Order in As-Te Glasses," Phys. Rev. B 48, 14650-14652, 1993.				
**	CQ7	Tranchant, et al., "Silver Chalcogenide Glasses Ag-Ge-Se: Ionic Conduction and Exafs Structural Investigation, Transport-structure Relations in Fast Ion and Mixed Conductors," Proceedings of the 6th Riso International symposium, 9-13, September 1985.				
**	CR7	Tregouet, et al., "Silver Movements in Ag ₂ Te Thin Films: Switching and Memory Effects," Thin Solid Films, 57, 49-54, 1979.				
**	CS7	Uemura, et al., "Thermally Induced Crystallization of Amorphous Ge0.4Se0.6," J. Non-Cryst. Solids, 117-118, 219-221, 1990.				
**	CT7	Uttecht, et al., "Electric Field Induced Filament Formation in As-Te-Ge Glass," J. Non-Cryst. Solids, 2, 358-370, 1970.				
**	CU7	Viger, et al., "Anomalous Behaviour of Amorphous Selenium Films," J. Non-Cryst. Solids, 33, 267-272, 1976.				
**	CV7	Vodenicharov, et al., "Electrode-limited Currents in the Thin-film M-GeSe-M System," Mat. Chem. And Phys., 21, 447-454, 1989.				
**	CW7	Wang, et al., "High-performance Metal/silicide Antifuse," IEEE Electron Dev. Lett., 13, 471-472, 1992.				
**	CX7	Weirauch, "Threshold Switching and Thermal Filaments in Amorphous Semiconductors," App. Phys. Lett., 16, 72-73, 1970.				
**	CY7	West, et al., "Equivalent Circuit Modeling of the Ag As0.24S0.36Ag0.40 Ag System Prepared by Photodissolution of Ag," J. Electrochem. Soc., 145, 2971-2974, 1998.				
**	CZ7	Zhang, et al., "Variation of Glass Transition Temperature, T _g , With Average Coordination Number, <m>, in Network Glasses: Evidence of a Threshold Behavior in the Slope dT _g /d<m> at the Rigidity Percolation Threshold (<m>=2.4)," J. Non-Cryst. Solids, 151, 149-154, 1992.				

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